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APPLICANTS

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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

JAPAN 2000-354471 11/21/2000

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance:				
Verified and Acknowledged	<u>Examiner's Signature</u> <u>12/23/04</u> <small>Initials</small>	COUNTRY JAPAN	DRAWING 18	CLAIMS 15 ✓	CLAIMS 2 ✓

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TITLE

Method and apparatus for correcting inclination of IC on semiconductor wafer

FILING FEE FEES: Authority has been given in Paper

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)
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